

<b>INFORMATION DISCLOSURE CITATION</b> <small>APR 03 2001</small> <small>PTO-1449</small>				ATTORNEY'S DKT NO. 040060-113	APPLICATION NO. 09/590,172	
				APPLICANT Lars BOHLIN		
				FILING DATE June 9, 2000	GROUP 2184	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,371,754	02/01/83	De et al.			
	4,700,292	10/13/87	Campanini			
	4,819,232	04/04/89	Krings			
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	Translation		
				Yes	No	
	0752656A2	01/08/97	EP			
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	W.W. Wesley Peterson and E.J. Weldon, Jr. "Error Correcting Codes", MIT Press, Cambridge, 1972"					
	C.L. Chen and M.Y. Hsiao "Error-Correcting Codes for Semiconductor memory Applications: A State-of-the-Art Review, IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 124-134.					
	J. Arlat and W.C. Carter "Implementation and Evaluation of a (b,k) - Adjacent Error-Correcting/Detecting Scheme for Supercomputer Systems", IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 159-169					
EXAMINER	DATE CONSIDERED <i>6/14/2004</i>					

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.